

2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): HSU, et al.

DOCKET: 728-208 (YOR9-2001-0270 US)

SERIAL NO.: 09/880,223

Group: 2814

FILED: June 13, 2001

Examiner: Rao, Shrinivas

FOR: T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR AND A
PSEUDO-TFT TRANSFER GATE AND A METHOD FOR FABRICATING
THE SAME

Dated: February 11, 2003

Assistant Commissioner for Patents
Washington, DC 20231

AMENDMENT TRANSMITTAL FORM

Sir:

Transmitted herewith is an amendment in the above-identified application.

[] Small entity status of this application under 37 C.F.R. §§1.9 and 1.27 has been established by a verified statement previously submitted.

[] A verified statement to establish small entity under 37 C.F.R. §§1.9 and 1.27 is enclosed.

[X] No additional fee is required.

(Col. 1)			(Col. 2)		(Col. 3)			SMALL ENTITY		OTHER THAN SMALL ENTITY		
CLAIMS REMAINING AFTER AMENDMENT			HIGHEST NO. PREVIOUSLY PAID FOR		PRESENT EXTRA		RATE		ADDIT. FEE OR RATE		ADDIT. FEE	
TOTAL	*8	MINUS	**28	=	0	X	9	\$	X	18	\$	0
INDEP.	*1	MINUS	***3	=	0	X	42	\$	X	84	\$	0
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM					X	140	\$	X	280	\$		
					TOTAL \$				OR TOTAL \$			
									ADDIT. FEE \$ 0			

* If the entry in Col. 1 is less than entry in Col. 2, write "0" in Col. 3.

** If the "Highest No. Previously Paid for" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest No. Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The Highest No. Previously Paid For" (Total or indep.) is the highest number found in the appropriate box in Col. 1 of a prior amendment or the number of claims originally filed.

[] Please charge Deposit Account No. 04-1121 in the amount of \$ _____. Two (2) copies of this sheet are enclosed.

[] A check in the amount of \$ _____ is enclosed.

[X] Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. §§1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. 04-1121. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 04-1121 therefor. TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.

DILWORTH & BARRESE, LLP
333 Earle Ovington Blvd.
Uniondale, NY 11553
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Respectfully submitted,

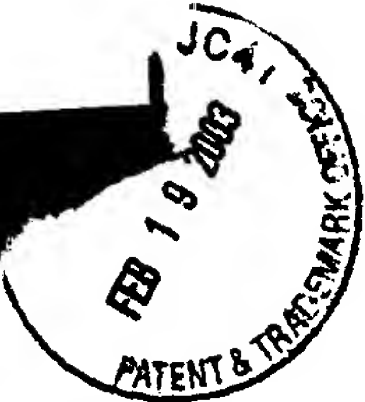
Paul J. Farrell
Reg No. 33,494
Attorney for Applicant

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

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Dated: February 11, 2003

Paul J. Farrell



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V. Brown
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RECEIVED US Patent Application No. 09/880,223
Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

FEB 20 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
TECHNOLOGY CENTER 2800

APPLICANTS: HSU, et al. GROUP ART UNIT: 2814
APPLN NO: 09/880,223 EXAMINER: Rao, Shrinivas
FILING DATE: June 13, 2001 DATED: February 11, 2003
FOR: **T-RAM CELL HAVING A
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AND A PSEUDO-TFT TRANSFER
GATE AND A METHOD FOR
FABRICATING THE SAME**

Assistant Commissioner for Patents
Washington, DC 20231

RESPONSE

In response to the Office Action of the United States Patent and Trademark Office
dated December 19, 2002, please amend this application as follows:

IN THE CLAIMS

Please add the following new claims:

29. (New) The array according to claim 13, wherein the transfer gate comprises a
horizontally stacked pseudo-TFT transfer gate.

AI

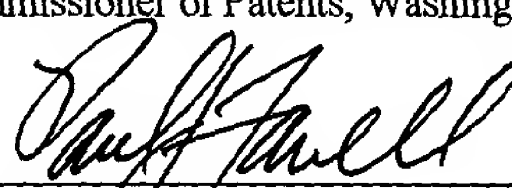
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02/26/2003 DSASFAI 00000023 500510 09880223

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Paul J. Farrell